

**Amendments to the Specification:**

Please amend the specification as follows:

**Please replace the paragraph bridging pages 1 and 2 (page 1, line 25 to page 2, line 9), with the following rewritten paragraph:**

In the liquid-crystal displays, there is a trend of increase in the number of pixels, and in turn, in the numbers of data lines and scan lines. The number of driving integrated circuits is also in the increasing trend. This trend, however, brings about an increase in manufacturing cost and aggravation of productivity. Therefore, there has been proposed a structure (hereinafter, "multiplexed image structure") in which one data line gives potentials to a plurality of pixel electrodes in time division. This makes it possible to ~~decreases~~ decrease the number of data lines and the number of the driving integrated circuits that are connected to the data lines.

**Please replace the paragraph bridging pages 27 and 28 (page 27, line 24 to page 28, line 23), with the following rewritten paragraph:**

While the present invention has been explained using two embodiments, the present invention is not limited to these embodiments and their modifications. A person skilled in the art could easily conceive of various embodiments and modifications based on the above embodiments. For example, the wiring structures of the pixel electrodes and thin-film transistors that are disposed on the TFT array substrate is not limited to that shown in, for example, Fig. 4. It is also possible to widely apply the present invention to a general image display that has a multiplexed image structure. Therefore, it is possible to realize a liquid-crystal display and the like that outputs a high-definition image by providing electrostatic shielding layers on the liquid-crystal displays that have a multiplex image structure described in Japanese Patent Application Laid-Open Publication No. 5-265045, Japanese Patent Application Laid-Open Publication No. 11-2837, Japanese Patent Application Laid-Open Publication No. 5-303114, Japanese Patent Application Laid-Open Publication No. 188395, and Japanese Patent Application Laid-Open Publication No. 2002-196357 (corresponding to Japanese Application No. 2000-373599). For example, Japanese Patent Application Laid-

Open Publication No. ~~2000-373599~~ 2002-196357 describes about an image display that has a structure that a first thin-film transistor and a second thin-film transistor are connected to pixel electrodes via respective source/drain electrodes, and the gate electrodes of the first and second thin-film transistors are connected to predetermined scan lines respectively. When the electrostatic shielding layers explained above are disposed in this structure, it is possible to suppress the generation of a striped pattern.